

METHOD AND APPARATUS FOR PROVIDING BUS ARBITRATIONS IN A DATA PROCESSING SYSTEM

Abstract of the Disclosure

A method and apparatus for providing bus arbitrations in a multiprocessor system is disclosed. A computer system includes a common bus that is shared by multiple cores, such as processors. A history of bus requests for the common bus made by the cores is stored in a bus request history table. In response to bus request made by the cores, the common bus is arbitrated according to information stored in the bus request history table by an arbiter.

Figures